

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,934,655 B2
APPLICATION NO. : 09/810932
DATED : August 23, 2005
INVENTOR(S) : Keith R. Jones et al.

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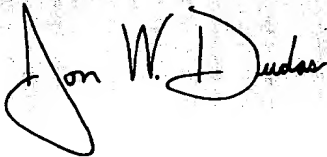
It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 11, between lines 55 and 56, please insert the following paragraph:

In one example method of operation, a continuous sequence of logic value 1's is provided to the input 500. The state of each register may be selectively loaded with a logical one or logical zero based on the desired sequence to be generated. When provided with a strong of logics one values, the generator outputs a unique string, or sequence, of 1's or 0's based on the values of the registers 510. In one embodiment, the values loaded into the registers are selected to form a primitive polynomial known to generate a maximal length sequence (M-sequence). The sequence will repeat through the $2^N - 1$ non-zero states.

Signed and Sealed this

Fifteenth Day of August, 2006

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looping initial "J" and a distinct "D".

JON W. DUDAS
Director of the United States Patent and Trademark Office